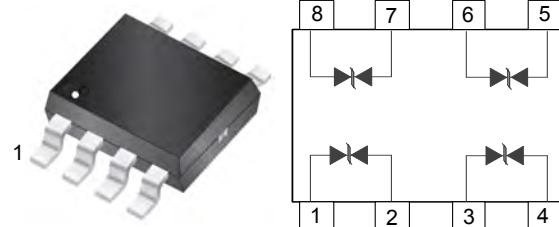


Features

- 240Watts peak pulse power ($t_p = 8/20\mu s$)
- SOIC-8 Package
- Low clamping voltage
- Low leakage current
- Low capacitance
- Protection four data/power line
- IEC 61000-4-2 (ESD) $\pm 30kV$ (air), $\pm 30kV$ (contact)
- IEC 61000-4-4 (EFT) 40A (5/50ns)
- IEC 61000-4-5 (Lightning) 16A (8/20 μs)
- 1.2/50us 12Ω 2KV(GbE PHY side)
- 10/700us 40Ω 4KV(GbE PHY side)



Mechanical Data

- **Case:** SOIC-8 (plastic package). Lead free; RoHS compliant; Halogen free
- **Molding Compound Flammability Rating:** UL 94 V-0
- **Terminals:** High temperature soldering guaranteed: 260 °C/10 sec. at terminals

Applications

- 10/100/1000 Ethernet
- Integrated magnetics/RJ-45 connectors
- LAN/WAN Equipment
- Security Cameras
- Industrial Controls
- Peripherals
- Notebooks & PC Computers

Absolute Maximum Ratings

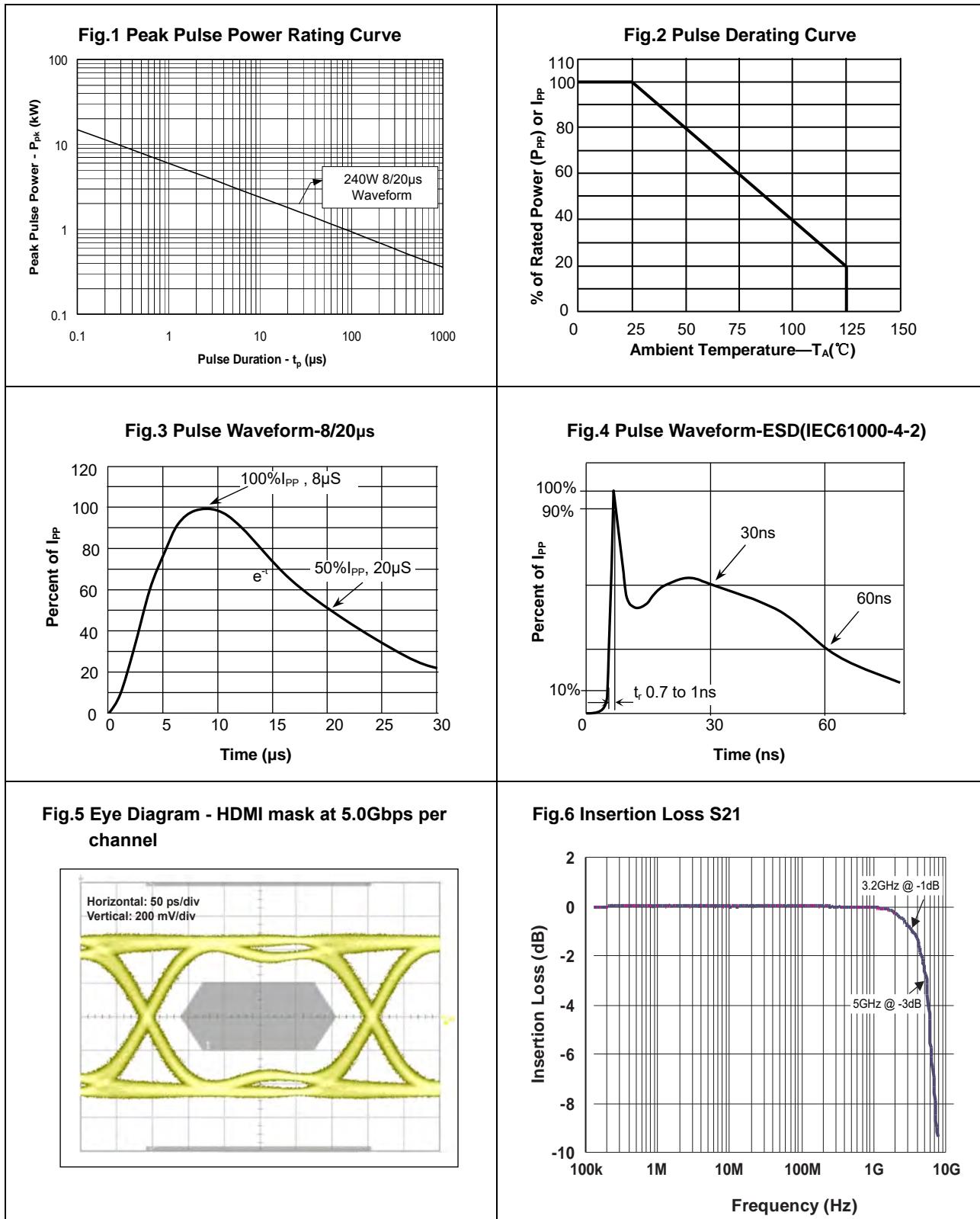
Ratings at 25 °C, ambient temperature unless otherwise specified

Parameter	Symbol	Value	Unit
Peak Pulse Power ($T_p=8/20\mu s$)	P_{PP}	240	W
ESD contact/air discharge (IEC-61000-4-2)	V_{ESD}	30/30	kV
Peak Pulse Current ($T_p = 8/20\mu s$)	I_{PP}	16	A
Junction Temperature	T_J	-55 to +125	°C
Storage temperature	T_{STG}	-55 to +150	°C

Electrical Characteristics

($T_A = 25$ °C unless otherwise specified)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Reverse stand-off Voltage	V_{RWM}				2.8	V
Reverse Breakdown Voltage	V_{BR}	$I_T=1mA$	4.0			V
Reverse Leakage Current	I_R	$V_R=2.8V$			0.5	µA
Clamping Voltage	V_C	$I_{PP}=16A, T_p=8/20\mu s$ Pin 1,8-2,7; Pin 3,6-4,5		8.5		V
Junction Capacitance	C_J	$V_R=0V, f=1MHz$ Pin 1,8-2,7; Pin 3,6-4,5		1.2		pF

Typical Characteristics ($T_{amb} = 25^{\circ}\text{C}$ unless otherwise specified)


Application Information

Lightning Surge Test Results for Gigabit Ethernet

During the metallic (line-to-line) surge test, the line being stressed is tied to the surge generator with the remaining lines tied together and connected to the generator ground. Current will flow through the line transformer transferring energy to the PHY side of the transformer. Figure 7 shows the test set-up for measuring the clamping voltage of the device. This set up is designed to test the surge in an actual gigabit Ethernet (GbE) circuit. Two 4.7 Ohm resistors are used for decoupling. The clamping voltage of the YELSO82T16AG for a metallic mode 4000V (100A) 10/700 μ s surge. The clamping voltage, measured at less than 13 volts, provides sufficient clamping margin to minimize electrical stress and is well below the failure voltage range of typical GbE PHY chips (Figure 8).

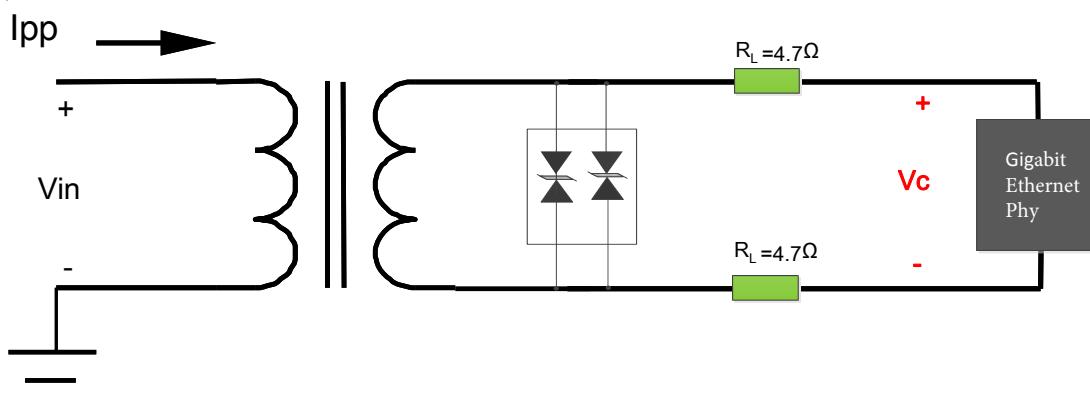


Figure 7 - Clamping Voltage Test Set-Up

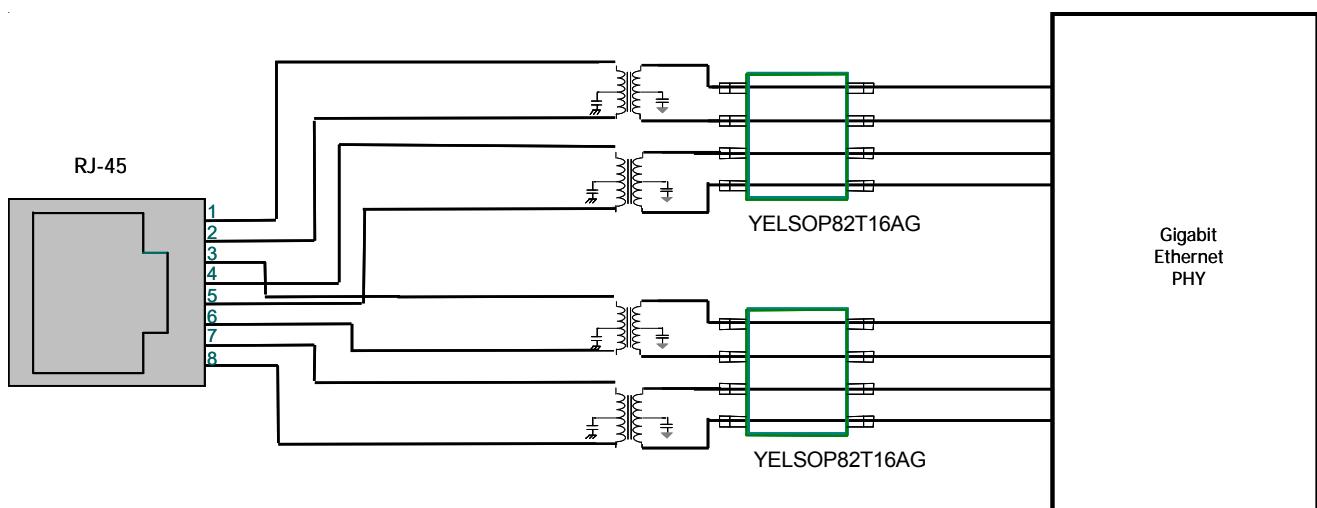


Figure 8 - GbE Protection to Lightning, ESD, and CDE

Package Dimensions

DIMENSIONS				
SYMBOL	INCHES		MILLIMETER	
	MIN	MAX	MIN	MAX
A	0.053	0.069	1.35	1.75
a1	0.004	0.010	0.10	0.25
a2	0.049	0.065	1.25	1.65
D	0.189	0.197	4.80	5.00
F	0.150	0.157	3.80	4.00
E	0.236BSC		6.00BSC	
b	0.012	0.020	0.31	0.51
e	0.050BSC		1.27BSC	
h	0.010	0.020	0.25	0.50
c	0.007	0.010	0.17	0.25
L	0.016	0.041	0.40	1.04
θ	0	8°	0	8°

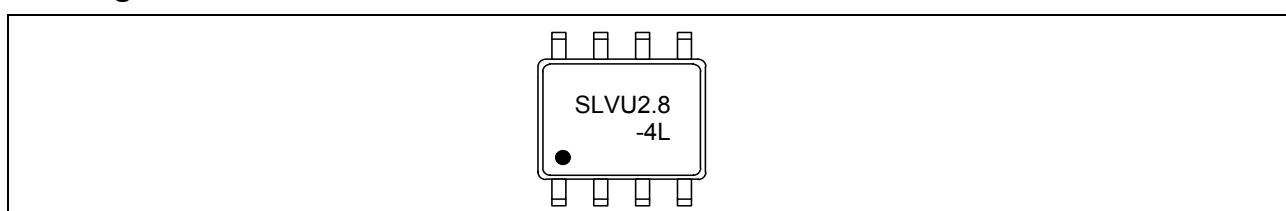
Pad Dimensions

DIM	INCHES	MILLIMETERS
C	(.205)	(5.20)
G	.118	3.00
P	.050	1.27
X	.024	0.60
Y	.087	2.20
Z	.291	7.40

NOTES:

1. THIS LAND PATTERN IS FOR REFERENCE PURPOSES ONLY. CONSULT YOUR MANUFACTURING GROUP TO ENSURE YOUR COMPANY'S MANUFACTURING GUIDELINES ARE MET.
2. REFERENCE IPC-SM-782A, RLP NO. 300A.

Marking



Ordering information

Order code	Package	Packaging option	Base quantity	Packaging specification
YELSOP82T16AG	SOIC-8	Tape and reel	2500pcs / reel	EIA STD RS-481